

POWER STABILITY MONITORING BASED ON VOLTAGE INSTABILITY PREDICTION APPROACH THROUGH WIDE AREA SYSTEM

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ABSTRACT

Nowadays, power systems are being forced to operate closer to its security limit due to current economic growth and the difficulties to upgrade the existing grid infrastructure. With the sudden increment of power demand, voltage instability problem has become a main concern to the power system operator because voltage instability has led or crucially contributed to some major blackouts throughout the world. Hence, methods for early warning and early prevention are required to prevent the power system from a collapse and led to blackout. Real-time voltage instability predictor method is applied on one of the load areas in Tenaga Nasional Berhad (TNB) network in this study. Voltage instability predictor calculation is performed according to the real-time measurement provided by Phasor Measurement Units (PMUs). The voltage instability predictor calculates the power margin and makes comparison to the pre-set margin threshold. Last but not least, the stability condition of the system is based on the comparison between the margin and the pre-set threshold. The main perception is to ensure the margin index always stay positive and suitable early prevention should be implemented when the index is close to zero.

Keywords: Discrete Kalman Filter (DKF), Phasor Measurement Units (PMUs), Radial Equivalent Independent (REI) Network, State Estimation, Thevenin Equivalent, Voltage Instability Predictor (VIP), Wide Area Measurement System (WAMS)

1. INTRODUCTION

Voltage instability is a non-linear phenomenon and closely related to the perception of maximum deliverable power limit of a transmission network (Taylor, 1994). Due to voltage instability occurs from the time duration of 10 sec up to few minutes' range of time periods (Kundur, 1994), voltage stability of a stressed power system should be monitored in real-time so that suitable control measures can be taken in a timely manner. Otherwise, voltage instability may lead to voltage collapse in the system or subsystem. Voltage collapse is

the evolution where the voltage falls to a low, unacceptable value and caused avalanched accompanying with voltage instability (Hill, 1993; Taylor, 1993; Kundur *et al.*, 2004).

Therefore, there are several well-known countermeasures were introduced in order to prevent voltage collapse, but the main constraint was lacked of information related to the current system state (Ajarapu and Meliopoulos, 2008). Hence, this problem will lead the system to incorrect or delayed correction actions and thus will give a chance of instability occurrence. Nowadays, with the advancement of Phasor

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Measurement Units (PMUs) technology, it provides fast and efficient ways to improve the current power system state estimation (Hurtgen and Maun, 2008). PMUs technology has been widely developed and researched since the past 32 years and PMUs have been deployed throughout different parts of the world such as North America, Europe, China and India (Chakrabarti *et al.*, 2009).

With continually increasing load demand (3.5% per annum for the next 10 years) and increasing fuel costs (natural gas as experienced in the last two quarters of financial year 2011) (TNB, 2011), there is an urgent need to improve operational efficiencies utilization of transmission and distribution assets and reduction of losses. Therefore, Tenaga Nasional Transmission (TNB-T) and Tenaga Nasional Berhad Research (TNB-R) have been joint cooperate on a 5-year research and development project on Wide-Area Intelligent System (WAIS) to enhance security and reliability of the power system network and at the same time to maximize the utilization of its assets. The introduction of Real-time Voltage Stability Margin (RTVSM) is one of the latest systems monitoring modules of WAIS to provide system operators information on proximity of the system to voltage collapse.

There are few factual of conducting this research on methods for early warning and prevention of voltage instability. Using PMUs technology performs the improvement of the current power system's state estimation by using PMUs technology. The implementation of an early-warning method on voltage instability for prevention purpose is going to be conducted. The investigations towards the availabilities of countermeasure are for prevention purpose. Last but not least is to improve the dispatching quality of the system towards stability borders.

The extent of the research was restricted to several limitations. The simulation results are mainly referred to the Tenaga Nasional Berhad 132 kV load area for 2-Bus system and 10-Bus system. Since the communication channels at some Tenaga Nasional Berhad (TNB) substations are not fully configured, RTVSM based on actual data streams are still unattainable at the moment. Hence, the results shown are based on input from simulations. In fact, the actual data from the applied PMUs in the system will never find any voltage collapse occurrences. The main reason is because the existing transmission lines are equipped with the protection and countermeasure systems to make an early prevention from the collapse. Subsequently, voltage collapse such as fault occurrence or tripping on the bus will be created with the Real-Time Digital Simulator (RTDS) in order to obtain the required PMUs data. Moreover, the RTDS

simulated data obtained from the TNB-R are in the form of voltage, current, active power and reactive power per phase values.

For the time being, TNB-R is implementing the current PMUs technology that can measure 50 samples per second, which is equal to 1 sample per 0.02 sec (Abdullah and Yusuf, 2008). The experimental performance of simulation was conducted at TNB-R with the 132 kV load area under investigation on 2-Buses System and 10-Buses System.

In this study, the implementation of voltage instability predictor calculation according to the real-time measurement provided by PMUs in TNB 132 kV load area on 2-Buses System and 10-Buses system. The voltage instability predictor calculates the power margin and makes comparison to the pre-set margin threshold. The stability of the system is then justified by comparing the margin and pre-set threshold.

This study is organized into five sections. Background of study is provided in section 2. Selections of methods are described in section 3. Results are provided and discussed in section 4. Finally, the significant conclusions for this study are concluded in section 5.

2. BACKGROUND OF STUDY

2.1. Wide area Measurement System (WAMS) in Malaysia

PMUs are increasingly being used as a major technology enable of the Wide Area Monitoring, Protection and Control (WAMPAC) system. PMUs are the most accurate and advanced time-synchronized technology, which provides voltage and current phasor and frequency information, synchronized with high precision to a common time reference provided by the Global Positioning System (GPS).

With the installation of PMUs eventually make a transition from a conventional SCADA-based measurement system to a more advance measurement system that will utilize synchronized measurements from geographically distant locations and increase the situational awareness by monitoring a wide area of the power system in real time (Minhat, 2008).

Meanwhile, the introduction of the Wide Area Monitoring System (WAMS) with PMU simulator was undertaken by TNB-R (Abdullah and Yusuf, 2008). The main objective was to build a reliable and accurate wide-area measurement system for Wide-Area Monitoring, Control and Protection R&D program to assist TNB to improve and secure the grid. The TNB WAMS system architecture is shown in **Fig. 1**.

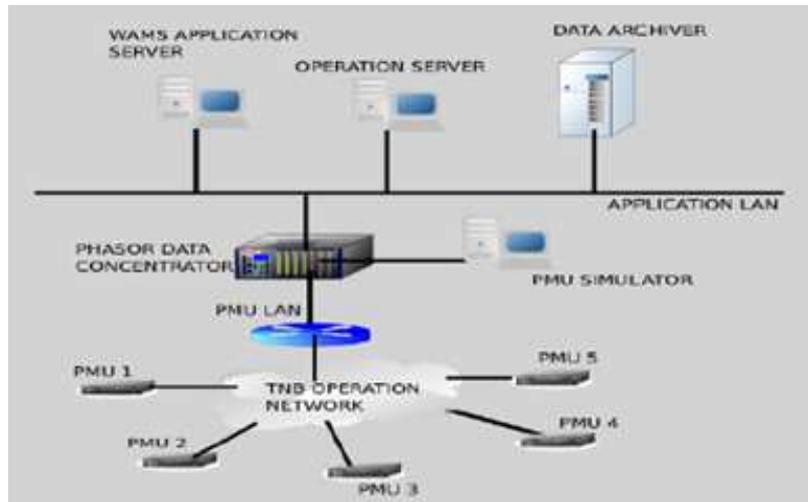


Fig. 1. Tenaga Nasional Berhad (TNB) wide Area Measurement System (WAMS) architecture

2.2. Methods for Voltage Stability Analysis

Voltage stability is continuous issues despite various researches were conducted a couple of decades ago. Several voltage collapse events initiated by voltage instability was reported in many parts of the globe (Lee and Lee, 1991). One of the local issues experienced in Malaysia is the major blackout event on 13 January 2005; which finally forced the local authority to shed some of the loads to balance the generation and distribution demands (Vaiman *et al.*, 2012). Many different analysis methods were proposed in the literature aiming to provide a better understanding of the phenomenon; to evaluate the systems operating conditions and to come up with appropriate control measures to keep systems from experiencing voltage instability situations.

2.2.1. P-V curve Method

This method is most widely used methods of voltage stability analysis. It gives the available amount of active power margin before the point of voltage instability (Taylor, 1994). A major disadvantage is that the power flow simulation will diverge near the nose or maximum power point on the curve. Another disadvantage is that generation must be realistically rescheduled as the area load is increased. Most importantly, the P-V curves are dynamics in real time, which mean that the curve will not be static (Warland and Holen, 2001).

2.2.2. Voltage Instability Predictor (VIP++) Method

This method used local measurement from simulated PMUs data to predict the distance to voltage collapse. VIP method was based on the assumption that voltage instability is closely related to a maximum load ability of a transmission network, thus the Thevenin impedance is equal to the apparent load impedance (Warland and Holen, 2001).

2.3. Voltage Stability Indicator

Many utilities company operation criterion still based on the magnitude of bus voltages. However, the voltage level is inadequate for voltage stability because the phenomenon is typically influenced by several other factors such as loading conditions, faults and reactive power resources (Canizares, 2002). In real world, it is impractical to compute the actual P-V curve of a dynamic system in real-time. Therefore, it is a greatest challenge to determine the under voltage relay setting in order to protect the system from voltage instability occurrences. A new technique to predict the behaviour of the system by treating the system as Thevenin equivalent network was proposed by (Vu *et al.*, 1999) with some modification is being used as one of the method in this research.

The maximum power transfer is simply determined based on voltage stability margin $(E_{th} - V_{th})^*$ and by the ratio of the load impedance to the Thevenin

impedance $|Z_{th}| = |Z_L|$ by referring to (Soliman *et al.*, 2003; Tsai and Wong, 2008). In summary,

Maximal power transfer Equation 1:

$$\bar{V} = \bar{E}_{th} - \bar{V}_L \tag{1}$$

Where:

\bar{V} = Voltage stability margin

\bar{E}_{th} = Vector of Thevenin voltage

\bar{V}_L = The vector of voltage at local bus

Maximal power transfer Equation 2:

$$|\bar{Z}_L| = |\bar{Z}_{th}| \tag{2}$$

Where:

$|\bar{Z}_L|$ = Vector of load impedance

$|\bar{Z}_{th}|$ = Vector of Thevenin impedance

The method of voltage instability load shedding using a local measurement unit was proposed by (Zhang *et al.*, 2012). While, the extension work which expressing the indicators in terms of the system critical voltage and maximum power that can be transmitted to the load provide meaningful interpretation to the system operators (Vu *et al.*, 2001). In this study, voltage stability indicator method is adopted as one of the techniques with some modification to investigate the voltage instability phenomenon in the TNB network.

3. SELECTION OF METHODS

3.1. Implementation of Radial Equivalent Independent (REI) Network

Due to major increasing power demand nowadays, power systems can be classified into large or very large systems, with a high degree of interconnectivity. Hence, the complexity of the power systems created a big challenge to the power system engineers to simplify the network for efficient system analysis (Gavrilas *et al.*, 2008).

By implementing of REI network, the analysis can be simplified by decreasing the size of the system to few nodes. Wide Area Power Systems (WAPS) are divided into three subsystems, which are Internal Power System (IPS), External Power System (EPS) and boundary power system or Boundary Nodes (BNs) as shown in Fig. 2.

The general circuit of REI network is shown in Fig. 3. The number of bus is denoted by “N”. The voltage and current denoted by V and I at the bus are respectively measured by PMUs. Besides, notation of S is representing the power transfer from the load centre. The V_G and V_R are represented by the ground bus voltage and fictitious bus voltage. The branch admittance is denoted by Y.

Based on nodal analysis, S_R and I_R which is the total sum of apparent power and current to the load, respectively, can be calculated by Equation 3:

$$S_R = \sum S_N = S_1 + S_2 + \dots + S_N \tag{3}$$

where, S_R is the fictitious bus apparent power Equation 4:

$$I_R = \sum I_N = I_1 + I_2 + \dots + I_N \tag{4}$$

where, I_R is the fictitious bus current

Therefore, the fictitious bus voltage and the branch admittance can be calculated by using Equation 5 and 6:

$$V_R = \frac{S_R}{I_R} \tag{5}$$

Where:

V_R = The fictitious bus voltage

S_R = The fictitious bus apparent power

I_R = The fictitious bus current

$$Y_R = \frac{I_R}{V_R} \tag{6}$$

Where:

Y_R = The branch admittance

I_R = The fictitious bus current

V_R = The fictitious bus voltage

3.2. State Estimation Using the Discrete Kalman Filter (DKF)

Thevenin equivalent network is shown in Fig. 4. The tracking process for the Thevenin equivalent is the most important for the voltage collapse detection. There are many methods to track the Thevenin parameters were applied previously. Five methods, including Cartesian coordinates, least square method, delta method, cumulative sum filter and polar coordinates were summarized by (Warland and Holen, 2001).

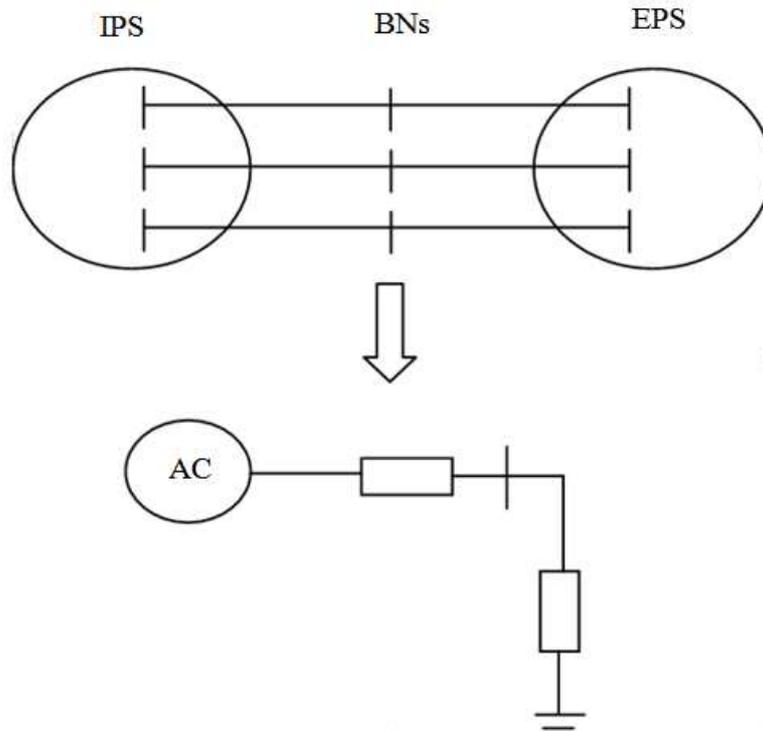


Fig. 2. WAPS and the simplified network system

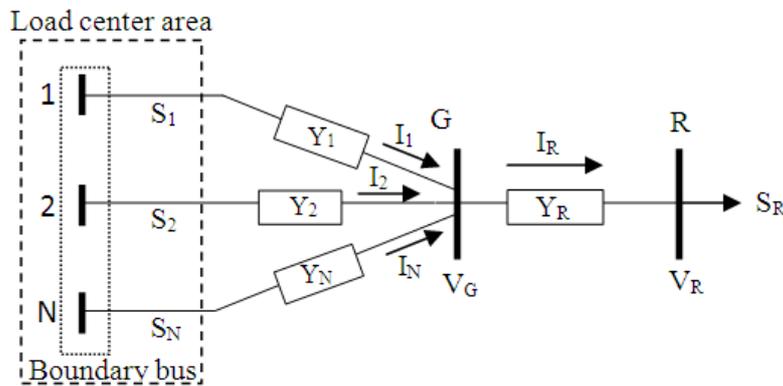


Fig. 3. General circuit of REI network

Application of Kalman filter method has become a popular technique discussed in several papers by (Vu *et al.*, 1999; Warland and Holen, 2001; Zhang *et al.*, 2012) due to the accuracy and stability to track the Thevenin parameters.

By applying Kirchhoff's Voltage Law (KVL) on the Thevenin equivalent of the system at Bus Z in Fig. 4 can be expressed by Equation 7 (Al-Ammari, 2002):

$$E_{TH} = V_L + (Z_{TH} * I_L) \tag{7}$$

Where:

E_{TH} = Thevenin voltage seen by Bus Z

V_L = The voltage of Bus Z

Z_{TH} = The Thevenin's equivalent impedance at Bus Z

I_L = The load current connected to the Bus Z

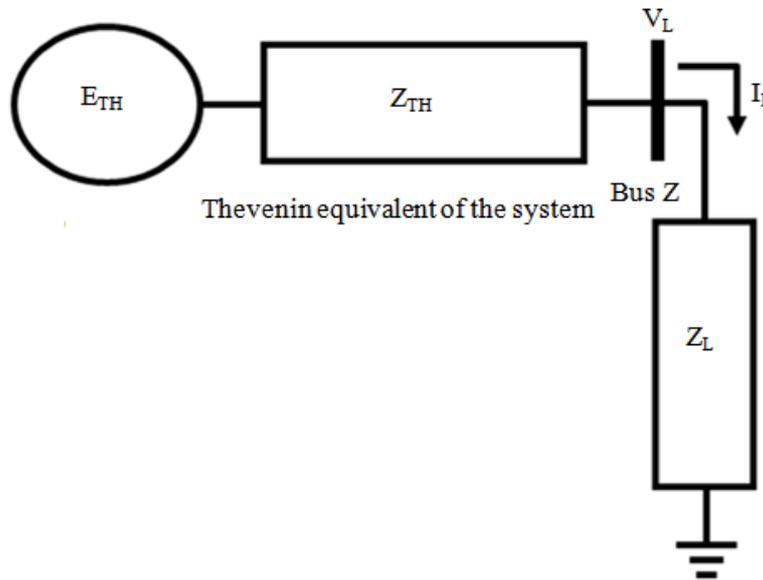


Fig. 4. Thevenin equivalent network

The voltage and load current connected to the Bus Z is represented in vector forms are shown in Equation 8 and 9:

$$\overline{V_L} = V_{\text{real}} + jV_{\text{imaginary}} = V_L \angle \alpha \quad (8)$$

$$\overline{I_L} = I_{\text{real}} + jI_{\text{imaginary}} \quad (9)$$

Where:

- $\overline{V_L}$ = The vector of voltage
- V_{real} = The real voltage of the complex number
- $V_{\text{imaginary}}$ = The imaginary voltage of complex number
- V_L = The magnitude voltage of the complex number
- α = The voltage angle of the complex number
- $\overline{I_L}$ = The vector of current flow to the load
- I_{real} = The real current of the complex number
- $I_{\text{imaginary}}$ = The imaginary current of the complex number

Meanwhile, the load impedance in terms of vector is shown in Equation 10:

$$\overline{Z_L} = R_L + jX_L = Z_L \angle \phi \quad (10)$$

Where:

- $\overline{Z_L}$ = Vector of load impedance

- R_L = The real resistance on the load of the complex number
- X_L = Imaginary reactance on the load of the complex number
- Z_L = The magnitude of the load impedance
- ϕ = The angle of phi of the load impedance

Thevenin voltage and impedance can be represented as a vector in Equation 11 and 12.

$$\overline{E_{TH}} = E_{\text{real}} + jE_{\text{imaginary}} = E_{TH} \angle \delta \quad (11)$$

$$\overline{Z_{TH}} = R_{TH} + jX_{TH} = Z_{TH} \angle \beta \quad (12)$$

Where:

- $\overline{E_{TH}}$ = Vector of Thevenin voltage
- E_{real} = The real voltage of the complex number
- $E_{\text{imaginary}}$ = The imaginary voltage of complex number
- E_{TH} = The magnitude Thevenin of voltage complex number
- δ = Magnitude current of the complex number
- $\overline{Z_{TH}}$ = Vector of Thevenin impedance
- R_{TH} = The real resistance on the Thevenin system of the complex number
- X_{TH} = The imaginary reactance on the Thevenin system of the complex number
- Z_{TH} = The magnitude of the Thevenin impedance

β = The angle of beta of the load impedance

By separating the real and imaginary parts, the equation can be formed in rectangular form as provided in Equation 13:

$$\begin{bmatrix} V_{\text{real}} \\ V_{\text{imaginary}} \end{bmatrix} = \begin{bmatrix} 1 & 0 & -I_{\text{real}} & I_{\text{imaginary}} \\ 0 & 1 & -I_{\text{imaginary}} & -I_{\text{real}} \end{bmatrix} \begin{bmatrix} E_{\text{real}} \\ E_{\text{imaginary}} \\ R_{\text{th}} \\ X_{\text{th}} \end{bmatrix} \quad (13)$$

Equation 13 is valid at any load bus on the system. In vector form and Equation 13 can be written as Equation 14:

$$\widehat{Z} = (H * \widehat{X}) + \widehat{V} \quad (14)$$

Where:

- \widehat{Z} = A 2-by-1 matrix voltage components vector
- \widehat{X} = A 4-by-1 state vector matrix to be identified
- H = A 2-by-4 observation matrix
- \widehat{V} = A 2-by-1 associated error matrix vector to be minimized

An estimation method using Kalman Filter is therefore developed. According to (Bishop and Welch, 2001), Kalman Filter can be divided into two, which are Discrete Kalman Filter (DKF) and Extended Kalman Filter (EKF). DKF method is implemented in this research. The DKF complete operation is shown in Fig. 5.

Where:

- \widehat{X}_k = The “prior estimate” (rough estimate before the measurement update correction)
- P_k = The “prior error covariance
- \widehat{X}_k = Estimate the x at time k
- P_k = necessary for k_1 (future) estimate together with \widehat{x}_k
- Kk = Kalman gain

3.3. Calculation for Maximum Power Transfer and Power Margin

From Thevenin equivalent network in Fig. 4, the maximum power transfer and critical voltage can be formulated as below (Foo, 2009).

The maximum real power transfer can be calculated by Equation 15:

$$P_{\text{max}} = \frac{ETH^2}{Z_{\text{TH}} * [2(1 + 2\cos(\beta - \phi))]} * \cos\theta \quad (15)$$

Where:

- P_{max} = Maximum real power transfer
- E_{TH} = Magnitude of Thevenin voltage
- Z_{TH} = The magnitude of Thevenin impedance
- β = The angle of Thevenin impedance
- ϕ = The angle of Load impedance
- θ = The (angle of bus voltage-angle of bus current)

The maximum reactive power transfer is Equation 16:

$$Q_{\text{max}} = \frac{ETH^2}{Z_{\text{TH}} * [2(1 + 2\cos(\beta - \phi))]} * \sin\theta \quad (16)$$

Where:

- Q_{max} = Maximum reactive power transfer
- E_{TH} = Magnitude of Thevenin voltage
- Z_{TH} = The magnitude of Thevenin impedance
- β = The angle of Thevenin impedance
- ϕ = The angle of Load impedance
- θ = The (angle of bus voltage-angle of bus current)

Power margin can be calculated from the different between maximum power transfer and the power transfer to the load as.

The real power margin Equation 17:

$$P_{\text{margin}} = P_{\text{max}} - PL \quad (17)$$

Where:

- P_{margin} = Real power margin
- P_{max} = Maximum real power transfer
- PL = Real power at the load

The reactive power margin Equation 18:

$$Q_{\text{margin}} = Q_{\text{max}} - Q_L \quad (18)$$

Where:

- Q_{margin} = Reactive power margin
- Q_{max} = Maximum reactive power transfer
- Q_L = Reactive power at the load

The apparent power margin Equation 19:

$$S_{\text{margin}} = S_{\text{max}} - S_L \quad (19)$$

Where:

- S_{margin} = Apparent power margin
- S_{max} = The maximum apparent power transfer
- S_L = Apparent power at load

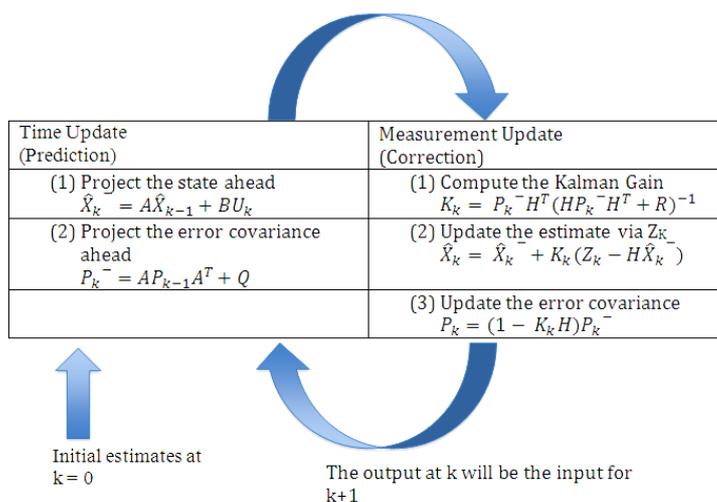


Fig. 5. A complete picture of the operation of the Discrete Kalman Filter (DKF)

Besides, with the predicted Thevenin parameters, Z_{th} values are achieved. While, Z_L can be obtain by using the PMUs data. According to (Vu *et al.*, 1999), voltage collapse will occur when Equation 20:

$$Z_{TH} = Z_L \tag{20}$$

Where:

Z_{TH} = The Thevenin impedance

Z_L = The load impedance

By having the estimated Thevenin impedance value, the minimum magnitude of the load impedance in a stable system can be defined. Hence, the early warning index can be defined in Equation (21) to monitor the condition on the power system:

$$\text{Index}_{\text{impedance}} = \frac{|Z_{TH}|}{|Z_L|} \tag{21}$$

More power will be drawn whenever the load impedance started to drop. By this, the early warning index also decreases indicating that the system is an edge near to the nose point of voltage collapse. The index is soon approaching 1 when the system is operating at the nose point. The main perception is to ensure the index should always stay less than 1 and suitable early prevention methods should be implemented when the index is getting closer to 1 (Foo, 2009).

4. SIMULATION RESULTS AND DISCUSSION

For more detailed analysis, the application is tested with simulation data from Real-time Digital Simulator (RTDS). This is to ensure that voltage instability would occur in the specified load area and in the meantime to estimate the performance of RTVSM application when the system is exposed voltage instability phenomena.

4.1. Application to TNB 132 kV Network (2 Bus System)

The analysis will be performed in one of the TNB load areas to examine the effectiveness of the voltage stability margin for 2 bus system. The 132 kV network of the area under investigation is shown in Fig. 6, where the incoming 275 kV supply is taken from Bus 1 and stepped down to 132 kV by two 180 MVA transformers, which connected to Bus 1 and Bus 2. All buses in this area are considered as the load bus. Load bus can be defined as transmission line feeding a certain load. However, this application will only consider Bus 5, Bus 6, Bus 7, Bus 8, Bus 9 and Bus 10 as the load centre.

Total load in the area is about 85 MVA and the power flows to the load centre are through Bus 5 and Bus 10. Two PMUs are installed to monitor voltage and current phasor at Bus 5 and Bus 10. This area was previously a rural area with very low load demand. However, loads have recently grown dramatically due to development of infrastructures and industrial estates.

The network is a ring network with the total line length almost 300 km.

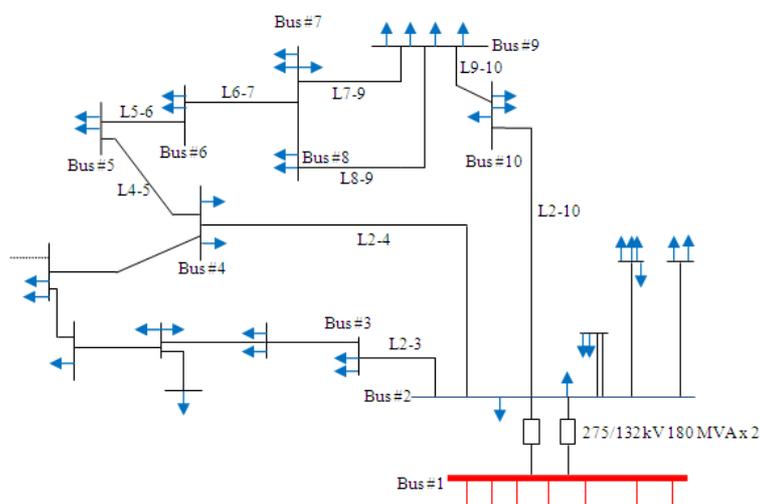


Fig. 6. Detail of TNB 132 kV load area under investigation

However, the network may become a long radial network if the line in L2-10 of the network is out of service. The worst contingency is when L2-10 is tripped. The transmission lines feeding the area will be facing severe overload and low voltage problem at the receiving end point, which is Bus 10.

4.2. Application to TNB 132 kV Network (10 Bus System)

The analysis will be performed in one of the TNB load areas to examine the effectiveness of the voltage stability margin for 10 bus system. All buses in this area are considered as the load bus. However, this application will only consider Bus 1, Bus 2, Bus 3, Bus 4, Bus 5, Bus 6, Bus 7, Bus 8, Bus 9 and Bus 10 as the load centre. Ten PMUs are installed to monitor voltage and current phasor at Bus 1, Bus 2, Bus 3, Bus 4, Bus 5, Bus 6, Bus 7, Bus 8, Bus 9 and Bus 10.

4.3. Case A: Results and Analysis for 2 Buses System

The voltage measurements and apparent power transfer at Bus 5 and Bus 10 by the simulated PMUs data during fault occurrence at 23.94 sec are shown in Table 1. By tripping of Bus 10 with ground fault is simulated followed by gradual increments of the demand at each bus after 23.94 sec as shown in Table 1. As expected, the voltage drops when the load is increased, which can be observed in Fig. 7 and 8.

After the line tripping at Bus 10, the power flow of Bus 10 drops to nearly zero as shown in Fig. 9. Therefore, at

that moment just only Bus 5 carries the power flow to the load centre at that moment as shown in Fig. 10.

The magnitudes of the measured load impedance and the estimated Thevenin impedance are shown in Fig. 11. The magnitude of load impedance decreases with the gradual load increment approaching the magnitude of the Thevenin impedance. The simulation started in a normal state without any occurrence until 23.94 sec where Bus 10 was tripped and causing the sharp rise of the Thevenin impedance while load impedance decreases.

Clearly from Table 2 that the two impedances, load and Thevenin almost come together at the point of collapse during 634.10 sec. They are, however, not becoming exactly equal because the Thevenin value is estimated and thus lags in the true value (the method involves a moving window). Nevertheless, the fact that two impedances are heading toward each other is evidence that Equation 20 holds.

From the observation in Table 3, P_L and P_{max} sharing the same value which is 1.493 p.u. at 634.10 sec, which is the same as the point where the load impedance crossing the Thevenin impedance.

Besides, the real power margin based on the difference between real power transfer and the limit is shown in Fig. 12. The real power margin is approaching zero at 634.1 sec in Table 4, which means that the system is on beyond the limit of voltage instability beginning. Operators at the utilities company are able to determine the status of the system based on the margin which roles as an indicator. After 634.1 sec in Fig. 12, the graph is no longer valid since the power flow solution after a voltage collapse diverges.

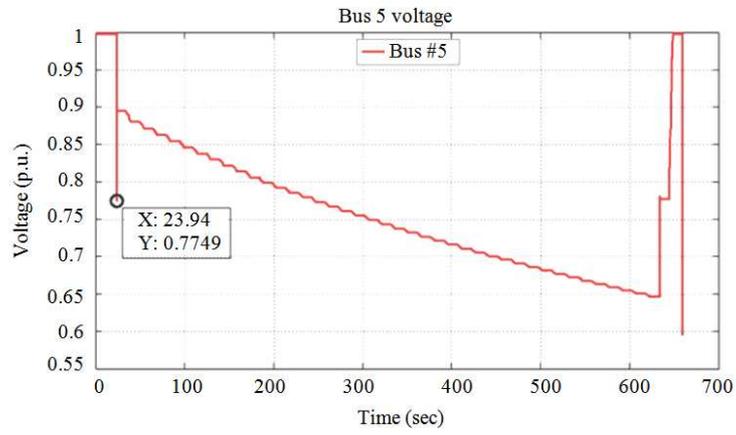


Fig. 7. Bus 5 voltage measurements

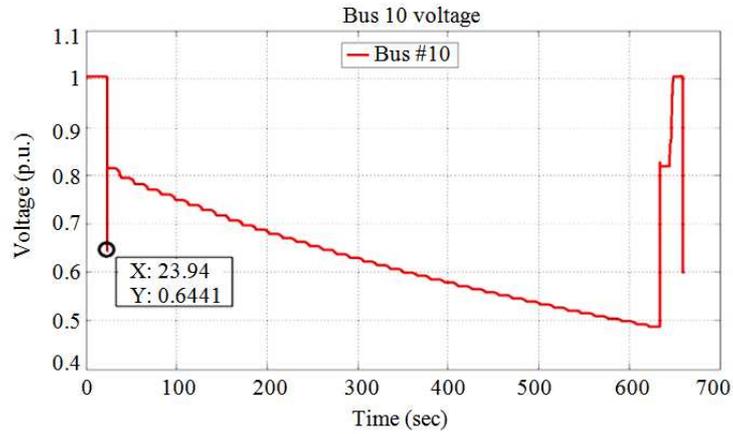


Fig. 8. Bus 10 voltage measurements

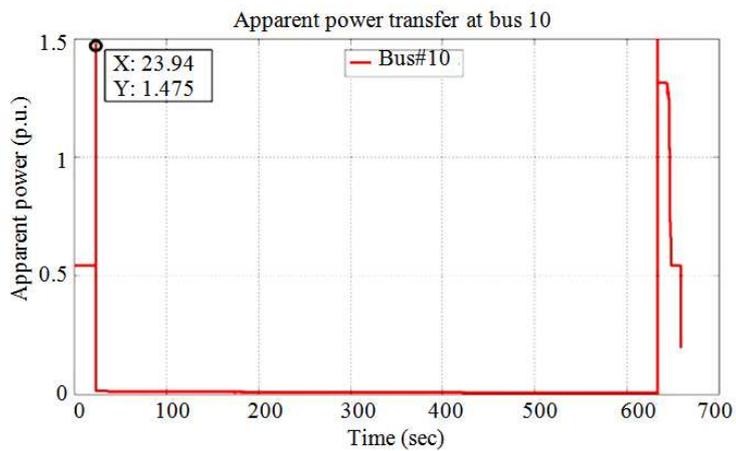


Fig. 9. Bus 10 apparent power



Fig. 10. Bus 5 apparent power

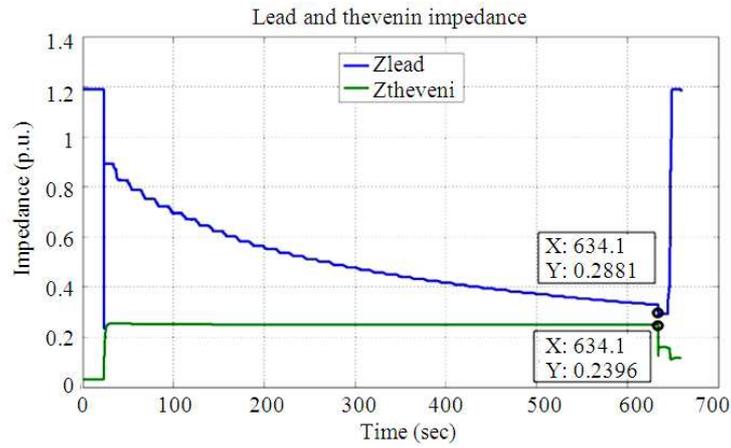


Fig. 11. Load impedance and Thevenin impedance merge at the point of collapse

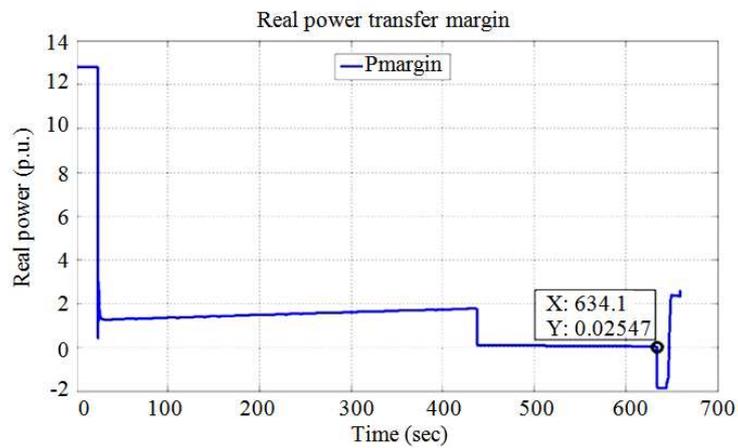


Fig. 12. Real power margin between the transfer and the load

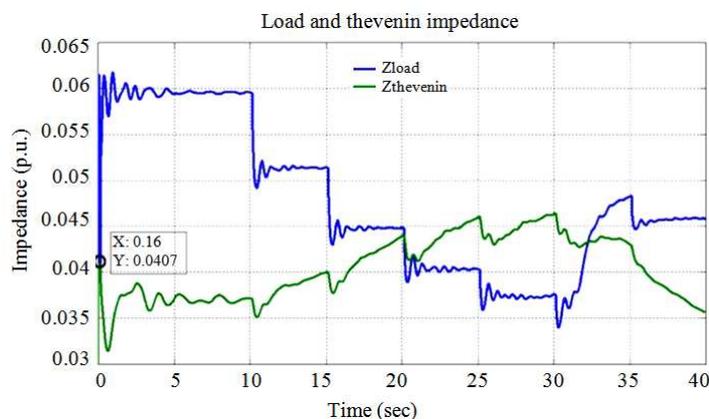


Fig. 13. Load impedance and Thevenin impedance merge at the point of collapse



Fig. 14. Real power margin between the transfer and the load

Table 1. The bus voltage and power transfer during fault occurred at 23.94 sec

Number of bus	Voltage fault	Power transfer
Bus number	Voltage (p.u.)	Apparent power (p.u.)
5	0.7749	0.4669
10	0.6441	1.4750

Table 2. Voltage collapse occurrence at 634.10 sec when load impedance crossing Thevenin impedance and load voltage crossing Thevenin voltage

Type of impedance	Impedance (p.u.)
Load impedance (Z_L)	0.7749
Thevenin impedance (Z_{th})	0.6441

Table 3. Real power transfer and limit when the system tends to collapse at 634.1 sec

Real power transfer	Real power (p.u.)
Real power (P_L)	1.493
Maximum Power (P_{max})	1.493

Table 4. Real power margin between maximum power and real power

Real power margin	Real power (p.u.)
Power margin (P_{margin})	0.02547

4.4. Case B: Results and Analysis for 10 Buses System

By tripping the line at Bus 3 with ground fault is simulated followed by gradual increment of the demand at each load bus after 0.1 sec. The voltage increments at Bus 1, Bus 2, Bus 3, Bus 4, Bus 5, Bus 6, Bus 7, Bus 8, Bus 9 and Bus 10 by the simulated PMUs data are listed in Table 5. After the line tripping at Bus 3, the power flow of Bus 3 drops to nearly zero as shown in Table 5. Therefore, at that moment just left Bus 1, Bus 2, Bus 4, Bus 5, Bus 6, Bus 7, Bus 8, Bus 9 and Bus 10 carry the power flow to the load centre at that moment.

The magnitudes of the measured load impedance and the estimated Thevenin impedance are shown in **Fig. 13**. The magnitude of load impedance decreases with the gradual load increment approaching the magnitude of Thevenin impedance. The simulation started in a normal state without any occurrence until 0.1 sec where Bus 3 was tripped and causing the sharp rise of the Thevenin impedance while load impedance decreases. In **Table 6**, the load and Thevenin impedance come together during 0.16 sec at the point of 0.0407 p.u.

From **Fig. 13**, the evidence shown that voltage collapse happened when load impedance crossing the Thevenin impedance. Once again, it is proven that the Equation 20 hold strongly.

From the observation in **Table 7**, P_L and P_{max} sharing the same value which is 16.44 p.u. at 0.16 sec, which is the same as the point where the load impedance crossing the Thevenin impedance.

The real power margin based on the difference between real power transfer and the limit is shown in **Fig. 14**. From the observation, the point of collapse is at 0.16 sec, which is the same as the point where load impedance crossing the Thevenin impedance.

Table 5. The bus voltage and power transfer during fault occurred at 0.1 sec

No. of bus Bus number	Voltage fault Voltage (p.u.)	Power transfer Apparent power (p.u.)
1	0.873	3.006
2	0.295	1.054
3	0.000	0.000
4	0.644	0.342
5	0.689	2.876
6	0.689	2.432
7	0.711	0.457
8	0.711	0.312
9	0.774	0.743
10	0.774	0.627

Table 6. Voltage collapse occurrence at 0.16 sec when load impedance crossing Thevenin impedance and load voltage crossing Thevenin voltage

Type of impedance	Impedance (p.u.)
Load impedance (Z_L)	0.0407
Thevenin impedance (Z_{th})	0.0407

Table 7. Real power transfer and limit when the system tends to collapse at 0.16 sec

Real power transfer	Real power (p.u.)
Real power (P_L)	16.44
Maximum Power (P_{max})	16.44

Besides that, the real power margin is approaching zero at 0.16 sec, which means that the system is on beyond the limit of voltage instability beginning. Operators at the utilities company are able to determine the status of the system based on the margin which roles as an indicator. The graph after 0.16 sec is no longer valid since the power flow solution after a voltage collapse diverges.

5. CONCLUSION

The investigation of a voltage instability phenomenon is based on TNB 132 kV load area. Simulation data from PMUs were collected from TNB-R. The conclusion can be concluded based on the findings of the investigation as following.

During normal loading condition, the margin index always stays positive. However, when any disturbance occurs, the ratio is definitely approaching zero and decreasing in non linear form. Hence, the main perception is to ensure the index always stay positive and suitable early prevention should be implemented when the index is close to zero.

Besides, whenever the system is approaching the point of collapse, any small changes in the power system will cause load voltage to have major drop, which had proven according to the behaviour of voltage collapse. The maximum power transfer and the load voltage were captured at the same point of collapse as the predicted Thevenin impedance crossing the load impedance.

Last but not least, the importance of real-time voltage stability margin which able to provide a proximity to voltage collapse based on the real power transfer margin, reactive power transfer margin and apparent power transfer margin.

Further extension of the current study will concentrate on the different types of method to estimate the Thevenin parameters. Besides, the investigation area can be further expand by using the IEEE 118 and 300 bus systems in order to achieve more influenceable results.

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