

# Artificial Neural Network Controller Based Distribution Static Compensator for Voltage Sag Mitigation

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Received 2013-02-18, Revised 2013-06-19; Accepted 2013-06-21

## ABSTRACT

Switching of loads, capacitors, along with the proliferation of power electronics equipment, non-linear loads in industrial, commercial and domestic applications have lead to power quality issues in the distribution system. Power quality issues such as voltage sag, voltage swell and harmonics, which are certainly major concerning issues in the present era. These issues can lead to failure or malfunction of the many sensitive loads connected to the distribution system, thus incurring a high cost for end users. Power quality problems are solved by advanced custom power devices. This study presents how the custom power device Distribution Static Compensator (D-STATCOM) is used to mitigate voltage sag and voltage harmonics in distribution system. Artificial Neural Network (ANN) controller based D-STATCOM is simulated in MATLAB-SIMULINK environment. Prototype model for single phase D-STATCOM is developed to verify the results. The simulation and hardware results show clearly the performance of the D-STATCOM in mitigating voltage sag and voltage harmonics in distribution system.

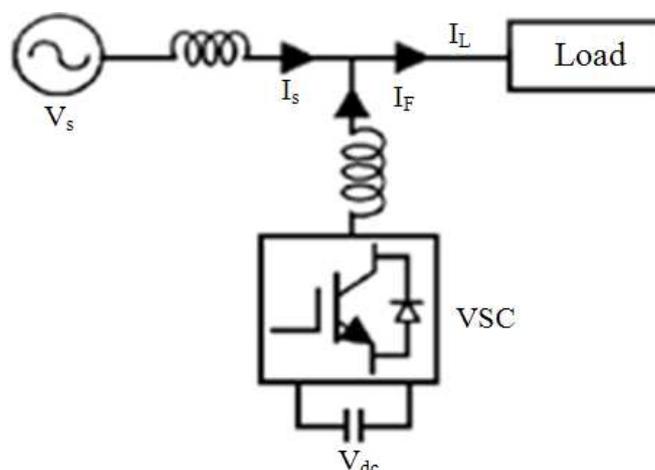
**Keywords:** Power Quality, D-STATCOM, Voltage Sag, IGBT, MATLAB/SIMULINK, Energy Storage System, Artificial Neural Network

## 1. INTRODUCTION

Voltage sag and voltage harmonics are the most important Power Quality (PQ) problems that many industries and utilities face it. Sensitive equipments used in modern industrial plants such as programmable logic controllers, process controllers and adjustable speed drives are not tolerate voltage sag and voltage harmonics (Dugan *et al.*, 2003; Singh *et al.*, 2009). The D-STATCOM has emerged as a promising device to provide voltage sag mitigation and harmonic control (Hosseini *et al.*, 2011). The single line diagram of D-STATCOM is shown in **Fig. 1**. The D-STATCOM is connected in shunt to the system (Babaei *et al.*, 2010).

The Voltage Source Converter (VSC) converts the dc voltage into a set of three-phase ac output voltage across the energy storage device. Output voltage from VSC is in phase and connected with the distribution System using the coupling transformer (Hosseini *et al.*, 2011). PWM-based control scheme which requires a current controller for generating the gating signals for IGBTs in VSC by using the reference and sensed source currents (Kannan and Rengarajan, 2012). Fuzzy-PI direct output voltage control strategy is presented to mitigate voltage sag in utility distribution system (Luo *et al.*, 2009a; 2009b). This study will investigate the capability of the ANN controller based D-STATCOM on mitigation of voltage sag and voltage harmonics.

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**Fig. 1.** Single line diagram of D-STATCOM

D-STATCOM is simulated in MATLAB/SIMULINK software. The simulation results show clearly ANN controller based D-STATCOM handled the situation without any difficulties and thereby keeping the load voltage balanced and constant at the nominal value and it also reduces voltage harmonics within the accepted range. Single phase hardware is implemented to verify the simulation results.

## 2. MATERIALS AND METHODS

**Figure 2** shows the single line diagram of distribution system under study. ANN controller based D-STATCOM is connected in the distribution system to mitigate voltage sag and voltage harmonics.

### 2.1. The ANN Controller

**Figure 3** shows the diagram of Artificial Neural Network (ANN) controller. The ANN controller used in this control system, consists of three neuron layers, the input, the hidden and the output layer. The ANN has three inputs ( $V_a, V_b, V_c$ ) and three outputs ( $V_a^*, V_b^*, V_c^*$ ). The output of ANN passes through a comparator and is compared with a carrier signal before applying as reference variable to the PWM generator. The ANN is trained by varying the weights  $W_{ij}$  and the biases  $B_j$ . The training criterion is taken as the mean square error of the ANN output with a value of 0.0001 and the error function is defined by the following Equation (1):

$$J = \sum_{i=1}^N e(i)^2 \quad (1)$$

**Table 1.** Simulated system parameters

Main supply voltage phase to phase	11 kV
Line Impedance	0.2 $\Omega$ , 1mH
Shunt transformer turns ratio	1:1
Filter Inductance	2 mH
Filter capacitance	1 $\mu$ F
Load resistance	0.001 $\Omega$
Load inductance	0.005 H
Load capacitance	1e-6 F
Line Frequency	50 Hz

where, N is the number of output neurons and  $e(i)$  is the instantaneous error between the actual and estimated values of the output. The training is completed when the value of j is less than 0.0001 (Mohaghegi *et al.*, 2005).

## 3. RESULTS AND DISCUSSION

### 3.1. Simulation Results

ANN controller based D-STATCOM is modeled and simulated by using MATLAB/Simulink. The learning process of ANN is developed in MATLAB, aided by the neural network toolbox. Simulated system parameters are given in **Table 1**.

At an initial stage non-critical load is connected to the distribution system from 0 to 0.05 sec. During this time rated voltage is available across load 1 i.e., no voltage sag. At 0.05 sec critical load is connected to the distribution system, the system voltage drops to 50% of its nominal value. The voltage sag occurs from 0.05 to 0.15 sec as shown in **Fig. 4**. To mitigate the voltage sag the required voltage is injected by the ANN controller based D-STATCOM from 0.06 to 0.14 sec as shown in **Fig. 5**. The Compensated voltage across the load is shown in **Fig. 6**.

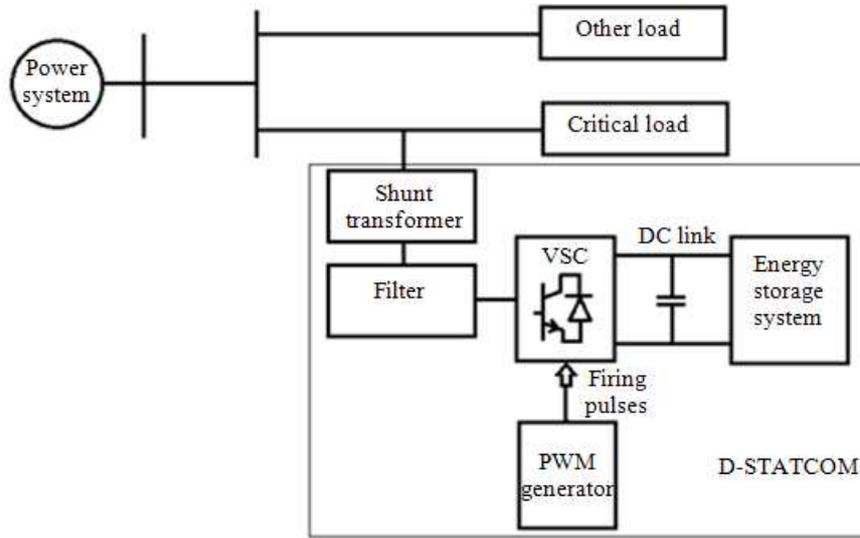


Fig. 2. Single line diagram of distribution system under study with D-STATCOM

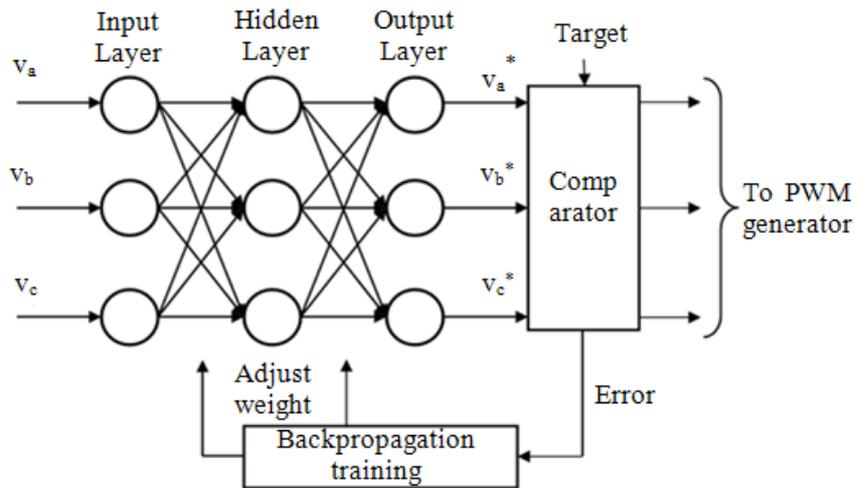


Fig. 3. ANN controller

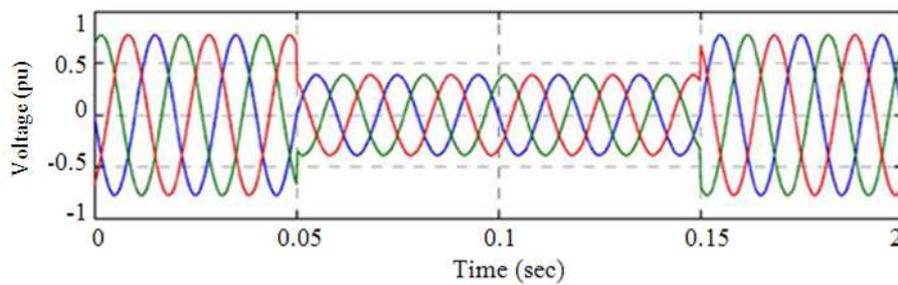


Fig. 4. Voltage under voltage sag condition

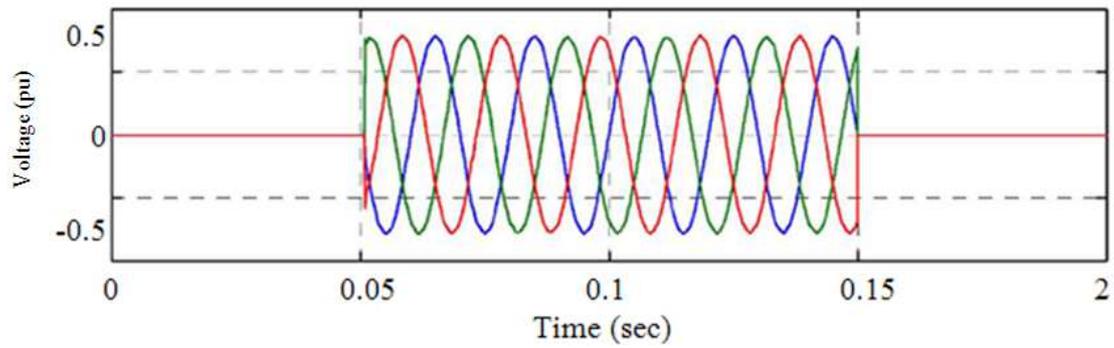


Fig. 5. Voltage Injected by D-STATCOM

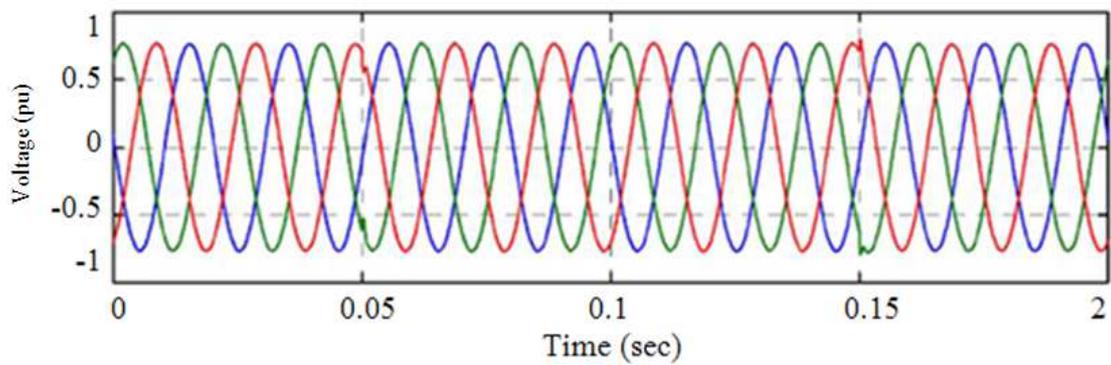


Fig. 6. Load voltage after compensation

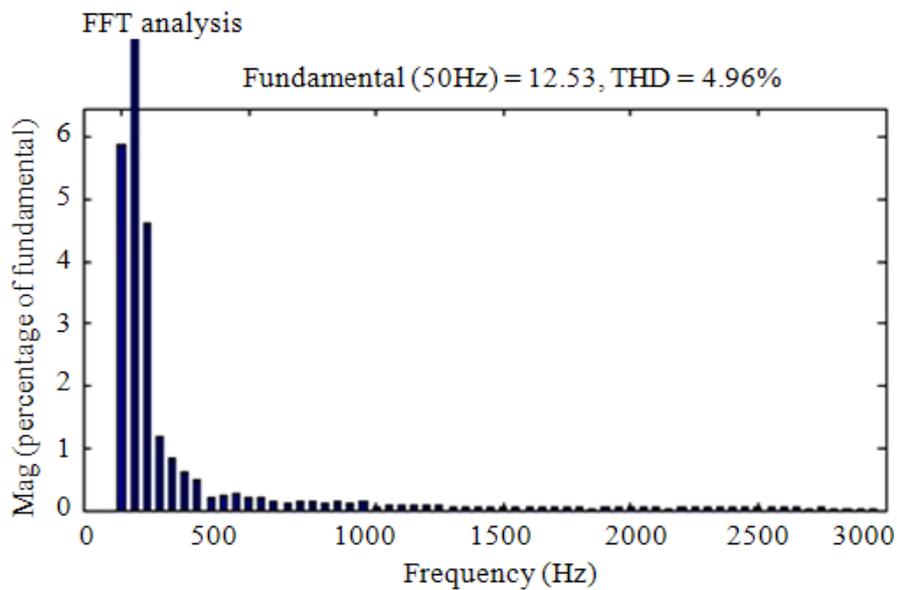
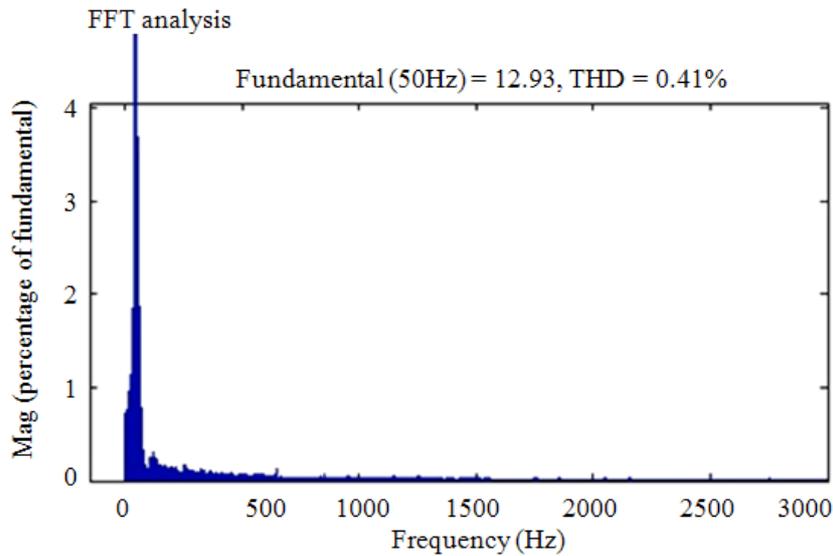
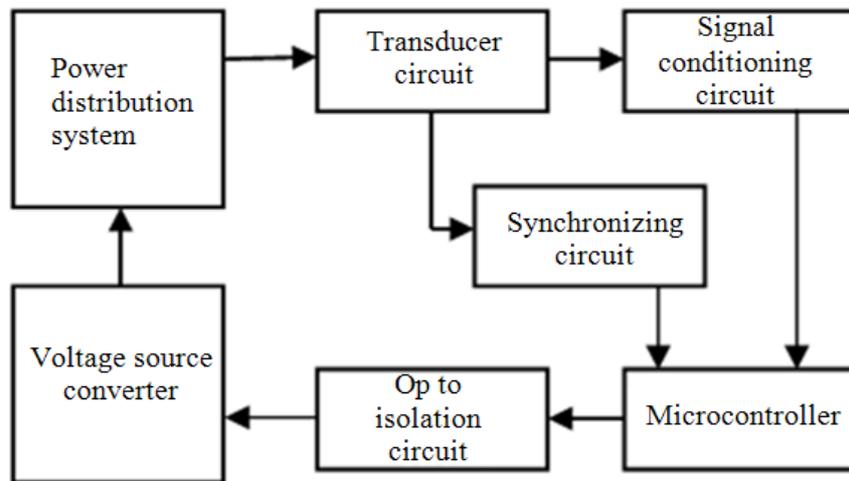


Fig. 7. FFT analysis without D-STATCOM



**Fig. 8.** FFT analysis with D-STATCOM



**Fig. 9.** Block diagram of single phase hardware model for D-STATCOM

The FFT analysis for the output voltage without D-STATCOM and with D-STATCOM is shown in **Fig. 7 and 8**. **Figure 7** gives FFT analysis without D-STATCOM. The THD value for this case is 4.96%. **Figure 8** gives FFT analysis with D-STATCOM. The THD value for this case is 0.41%. Thus THD is reduced from 4.96 to 0.41% by using ANN controller based D-STATCOM.

The simulation results show the effectiveness of D-STATCOM to mitigate voltage sag and voltage harmonics in the distribution system. THD values with and without D-STATCOM are shown in **Table 2**.

**Table 2.** THD values without and with D-STATCOM

THD	Without D-STATCOM	With D-STATCOM
	4.96%	0.41%

### 3.2. Description of Hardware Modules

**Figure 9** shows block diagram of single phase hardware model for D-STATCOM.

The major components of microcontroller based D-STATCOM is given as follows:

- Transducer circuit
- Signal conditioning circuit
- Synchronizing circuit
- Microcontroller
- Opto isolation circuit
- Voltage source converter

The detailed description of different parts of the hardware is given below.

### 3.3. Transducer Circuit

The power quantities are sensed and converted into low level signal in transducer circuit.

### 3.4. Signal Conditioning Circuit

The low level signal from transducer circuit is converted into 0-5 V range to make them compatible with microcontroller.

### 3.5. Synchronizing Circuit

The PCC voltages are distorted and not possess sharp zero crossing. So it is difficult to find zero crossing from

PCC voltages. Hence synchronizing circuit is required to extract and synchronize desired signal.

### 3.6. Microcontroller

It is a small computer on a single integrated circuit containing processor, memory and programmable input/output peripherals. From the signal conditioning circuit the signal is given to the microcontroller in which the artificial neural network algorithm takes care of the PWM signal.

### 3.7. Opto Isolation Circuit

Opto couplers are used to provide isolation between control circuit and power circuit.

### 3.8. Voltage Source Converter

Output of the microcontroller is PWM signals. These PWM signals are used as the gate trigger pulses for voltage source converter.

Output from the voltage source converter is applied to the injection transformer which is in parallel with the line to mitigate voltage sag and voltage harmonics.

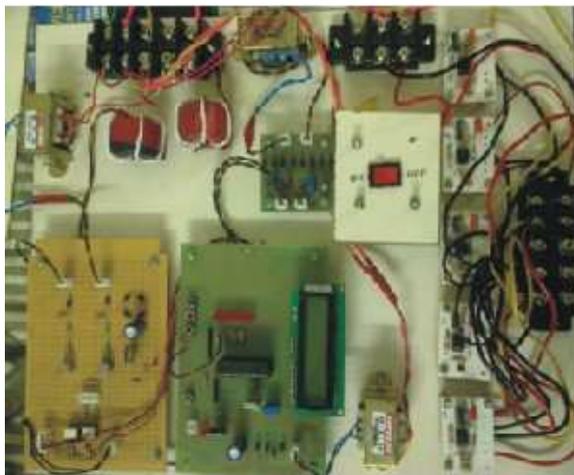


Fig. 10. Single phase hardware model of D-STATCOM

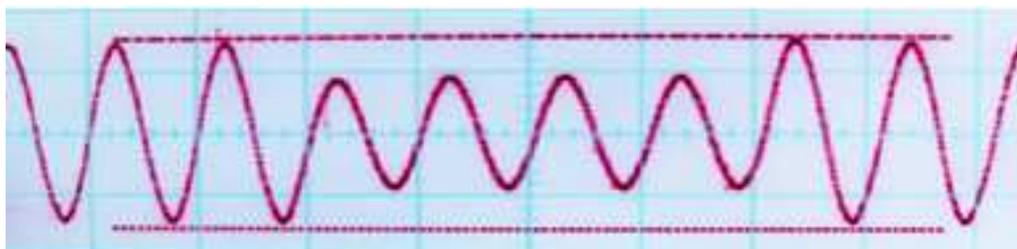


Fig. 11. Voltage under voltage sag condition

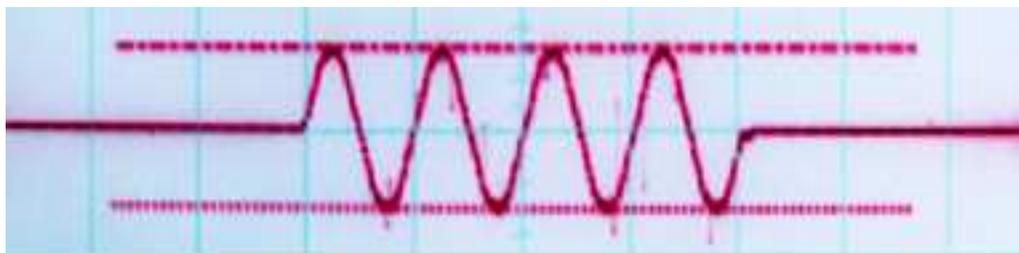


Fig. 12. Voltage injected by D-STATCOM

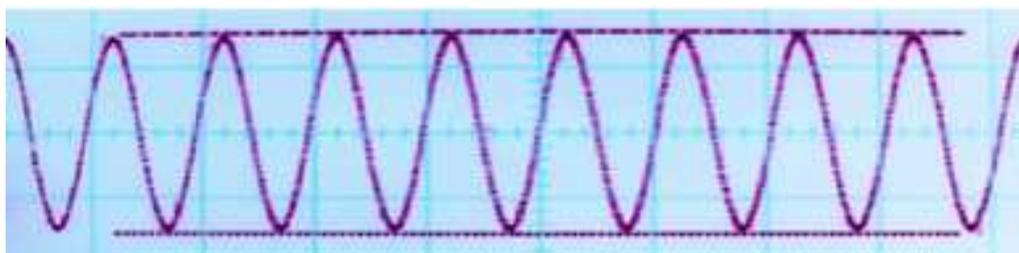


Fig. 13. Load voltage after compensation

### 3.9. Experimental Results

The photograph of the single phase hardware for D-STATCOM to mitigate voltage sag is shown in Fig. 10. The output voltage of the system without D-STATCOM is shown in Fig. 11. From the Fig. 11 it is identified that 50% sag occurs without D-STATCOM. When sag occurs DSTATCOM inject appropriate voltage i.e., 50% voltage to power system as shown in Fig. 12. The load voltage after compensation is shown in Fig. 13. It can be seen that the output voltage across the load is nearly nominal value (i.e., 1 p.u.) and sinusoidal.

### 4. CONCLUSION

This study presents how the custom power device D-STATCOM is used to mitigate voltage sag and voltage harmonics in distribution system. ANN controller based D-STATCOM is simulated in MATLAB-SIMULINK environment. Three multilayer neural network is used to identify and regulate the voltage across sensitive load. Microcontroller based hardware for single phase D-STATCOM is developed to verify the results. The simulation and experimental results shows the effectiveness of D-STATCOM to mitigate voltage sag and voltage harmonics in distribution system.

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